

PATENT NUMBER

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| O.I.P.E. |    | PATENT DATE |
| KS       | LM | WJ          |
| SCANNED  | LM | WJ          |

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|------------------------------|------------|--------------|-----------------|-------------------------------------|----------------------|
| APPLICATION NO.<br>09/966157 | CONT/PRIOR | CLASS<br>438 | SUBCLASS<br>618 | ART UNIT<br>2812<br><del>2850</del> | EXAMINER<br>Guerrero |
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TITLE  
Mask layer and interconnect structure for dual damascene semiconductor manufacturing

PTO-20  
12/98

[illegible]

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|--|--|-------------|------------|---|----------------------|
| <input type="checkbox"/> <b>TERMINAL<br/>DISCLAIMER</b>  | <b>DRAWINGS</b>                              |             |            | <b>CLAIMS ALLOWED</b>                     |                      |
|  | Sheets Drwg.                                 | Figs. Drwg. | Print Fig. | Total Claims                              | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.   | _____<br>(Assistant Examiner) (Date)         |             |            | <b>NOTICE OF ALLOWANCE MAILED</b><br><br> |                      |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____<br>_____<br>_____ | _____<br>(Primary Examiner) (Date)           |             |            | <b>ISSUE FEE</b><br>Amount Due Date Paid  |                      |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.  | _____<br>(Legal Instruments Examiner) (Date) |             |            | <b>ISSUE BATCH NUMBER</b>                 |                      |

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